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10/01/03

Sir:

Transmitted herewith for filing is the patent application of

Inventors: Jau-Wen Chen, Yoon Huh and Peter Bendix

For: **SUBSTRATE-BIASED I/O AND POWER ESD
PROTECTION CIRCUITS IN DEEP-SUBMICRON
TWIN-WELL PROCESS**

Enclosed are:

- ☒ Six (6) sheets of formal drawings.
- ☒ An executed Declaration and Power of Attorney.
- ☒ An Application Data Sheet.
- ☒ An executed Assignment.
- ☒ An Information Disclosure Statement.

The filing fee has been calculated as shown below:

	(Col. 1)	(Col. 2)
FOR:	NO. FILED	NO. EXTRA
BASIC FEE		
TOTAL CLAIMS	20 - 20 =	* 0
INDEP. CLAIMS	2 - 3 =	* 0
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIM PRESENTED		

*If the difference in Col. 1 is less than zero, enter "0" in Co. 2

SMALL ENTITY	
RATE	FEE
	\$385.00
x 9 =	\$.00
x 40 =	\$.00
+ 135 =	\$.00
TOTAL	\$.00

OTHER THAN A SMALL ENTITY	
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x 80 =	\$.00
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Dated: October 1, 2003

James R. Foley
Attorney of Record

Reg. No. 39,979

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